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Pradeep Teregowda): Three dimensional (3D) Network-on-Chip (NoC) architectures Kostas Siozios {Exploration of Alternative Topologies for

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in NoC, in Designing 2D and 3D Network-on-Chip K. Siozios, I. Anagnostopoulos, D. Soudris, on 3D Network-on-Chip architectures,

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